

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: Appn. Ser. No. Not Yet Assigned : Art Unit Not Yet Ass'd

Filed Herewith : Exr. Not Yet Ass'd

Inventors Coteus et al. : Atty Dkt No. YOR919980423US3

For: SEMICONDUCTOR RECESSED MASK INTERCONNECT TECHNOLOGY

INFORMATION DISCLOSURE TRANSMITTAL

Commissioner For Patents
P.O. Box 1450
Arlington, Va. 22313 - 1450

Sir:

Please find enclosed a listing (PTO Form 1449 type) of references with copies which may be relevant to the above identified application. This listing of references is not a representation that a search has been made, that better references than listed do not exist, or that other references are not applicable. Moreover this listing does not constitute any admission by applicants or applicants' attorney that the information provided herein is necessarily prior art to applicants invention.

Respectfully Submitted

Alvin J. Riddles 10/16/03
Alvin J. Riddles
Reg. No. 17862

LIST OF REFERENCES CITED BY APPLICANT
(Use several sheets if necessary)

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COTEUS ETAL

FILING DATE

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA	3810577	5/14/74	DRESCHER ETAL	235	153	
AB	4369511	1/19/83	KIMURA ETAL	371	21	
AC	4649475	3/10/87	SCHUENEMAN, J.H.	364	200	
AD	5742753	4/21/98	NORDSIECK ETAL	395	182.09	
AE						
AF						
AG						
AH						
AI						
AJ						
AK						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
AL							
AM							
AN							
AO							
AP							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

AR	"256MB DOUBLE DATA RATE SYNCHRONOUS DRAM", IBM BROCHURE, 33L8026 FK5424, 1/99, 67 PAGES
AS	"16Mx72 2 BANK REGISTERED DDR SDRAM MODULE", IBM BROCHURE, 19L7204 FIVER, 11/98, PAGES 1-8
AT	

AMINER

DATE CONSIDERED